Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("20040057173"):PN:	US-PGPUB; USPAT	OR	OFF	2005/05/06 07:08
L2	1138	(transceiver transmiter receiver) same semiconductor near2 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON .	2005/05/06 07:24
L3	75	2 and (overvoltage voltage near2 surge electrostatic ESD spike)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ÖR	ON	2005/05/06 07:28
L4	9526	(cellular wireless near2 communication transceiver transmiter receiver) same (IC semiconductor near2 chip)	US-PGPUB; USPAT	OR	ON	2005/05/06 07:28
L5	910	4 and (overvoltage voltage near2 surge electrostatic ESD spike)	US-PGPUB; USPAT; USOCR; EPO; JPO;	OR	ON	2005/05/06 07:28
L6	109	4 same (overvoltage voltage near2 surge electrostatic ESD spike)	DERWENT US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:39
L7.	3569	361/56,91.1,111.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ÖN	2005/05/06 07:39
L8	22	4 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:41
L9	17	8 and (overvoltage voltage near2 surge electrostatic ESD spike)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:39
L10	3543	361/117-120.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:40
L11	4	4 and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:41
L12	720	transmitter and receiver and esd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:43
L13	537	12 and (IC integrated near2 circuit chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:41

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L14	1133	(negative near2 electrostatic) and (positive near2 electrostatic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:42
L15	2	13 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:42
L16	75	transmitter same receiver same esd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 07:43
L17	3643	(361/56-58).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/06 08:08
L18	21	4 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/05/06 08:08